

WHAT IS CLAIMED IS:

1. A method comprising:

5 in response to a cold reset in a computer system, initializing a plurality of
 indications in a nonvolatile memory to a first state, wherein each of the
 plurality of indications is assigned to a respective one of a plurality of
 tasks to be executed on one or more processors of the computer system;
 and
10 executing a first task of the plurality of tasks, the executing comprising changing a
 first indication of the plurality of indications to a second state, wherein the
 first indication is assigned to the first task.

15 2. The method as recited in claim 1 further comprising executing a second task of the
 plurality of tasks, the executing comprising changing a second indication of the plurality
 of indications to the second state, wherein the second indication is assigned to the second
 task.

20 3. The method as recited in claim 1 wherein the executing further comprises initially
 checking that the first indication is in the first state.

 4. The method as recited in claim 3 wherein the executing comprises exiting the first task
 if the first indication is not in the first state.

25 5. The method as recited in claim 1 wherein the executing further comprises determining
 if a warm reset has occurred, and exiting the first task if the warm reset has not occurred.

 6. The method as recited in claim 5 wherein the determining comprises checking a warm

reset indication stored in a register in the computer system.

7. The method as recited in claim 1 wherein the executing further comprises causing a warm reset subsequent to changing the first indication.

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8. The method as recited in claim 1 further comprising, in response to the cold reset, changing at least a cold reset indication and a warm reset indication in a register in the computer system.

10 9. A computer accessible medium comprising:

15 a first one or more instructions which, when executed in response to a cold reset in a computer system, initialize a plurality of indications to a first state, wherein each of the plurality of indications is assigned to a respective one of a plurality of tasks to be executed on one or more processors of the computer system; and

20 a first task of the plurality of tasks comprising a second one or more instructions, wherein the second one or more instructions, when executed, change a first indication of the plurality of indications to a second state, wherein the first indication is assigned to the first task.

25 10. The computer accessible medium as recited in claim 9 further comprising a third one or more instructions included in a second task of the plurality of tasks, wherein the third one or more instructions, when executed, change a second indication of the plurality of indications to the second state, wherein the second indication is assigned to the second task.

11. The computer accessible medium as recited in claim 9 wherein the first task further

comprises a fourth one or more instructions which, when executed, initially check that the first indication is in the first state.

5 12. The computer accessible medium as recited in claim 11 wherein the fourth one or more instructions which, when executed, exit the first task if the first indication is not in the first state.

10 13. The computer accessible medium as recited in claim 9 wherein the first task further comprises a fifth one or more instructions which, when executed, determine if a warm reset has occurred, and exit the first task if the warm reset has not occurred.

14. The computer accessible medium as recited in claim 13 wherein the fifth one or more instructions determine if the warm reset has occurred by checking a warm reset indication stored in a register in the computer system.

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15. The computer accessible medium as recited in claim 9 wherein the first task further comprises a sixth one or more instructions which, when executed, cause a warm reset subsequent to changing the first indication.

20 16. The computer accessible medium as recited in claim 9 further comprising a seventh one or more instructions which, when executed in response to the cold reset, change at least a cold reset indication and a warm reset indication in a register in the computer system.

25 17. The computer accessible medium as recited in claim 9 comprising a first medium comprising the first one or more instructions and the first task and a second medium storing the plurality of indications.

18. The computer accessible medium as recited in claim 17 wherein the second medium

comprises a nonvolatile memory.

19. A computer system comprising:

5 a processor; and

a computer accessible medium coupled to the processor, the computer accessible medium comprising:

10 a first one or more instructions which, when executed in response to a cold reset in a computer system, initialize a plurality of indications to a first state, wherein each of the plurality of indications is assigned to a respective one of a plurality of tasks to be executed on one or more processors of the computer system; and

15 a first task of the plurality of tasks comprising a second one or more instructions, wherein the second one or more instructions, when executed, change a first indication of the plurality of indications to a second state, wherein the first indication is assigned to the first task.
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